## HODUCAL ZANDUNDL



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

priority Application Serial No	
priority Filing Date	August 7, 2000
Inventor	Gurtej S. Sandhu et al.
Assignee	. Micron Technology, Inc.
priority Group Art Unit	2813
priority Examiner	Laura M. Schillinger
Attorney's Docket No	MI22-1897
Title: Methods of Forming Transistors (As Amended)	

## PRELIMINARY AMENDMENT

To:Box Patent Application

Assistant Commissioner for Patents

Washington, D.C. 20231

From:

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In the Specification

Replace the title with -- Methods of Forming Transistors--.

At page 1, before the "Technical Field" section, please insert:

-- CROSS REFERENCE TO RELATED APPLICATION

This patent resulted from a divisional application of U.S. Patent

Application Serial No. 09/633,556, which was filed on August 7, 2000.-- NOW US. Pat. No. 6,660657

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